

IN THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A display device comprising a display panel having a plurality of display pixels with emissive display elements; and duty cycle control means for varying a fraction of a frame period during which said display pixels emit light in dependence on a total brightness level for a frame of an image to be displayed on said display panel, wherein the control means are adapted to determine the fraction of the frame period in dependence on the total brightness level of the image during a previous frame period.

2. (Previously Presented) The display device according to claim 1, wherein said control means are adapted to increase said fraction if a decrease of said total brightness level is determined.

3. (Previously Presented) The display device according to claim 1 wherein said control means are adapted to decrease said fraction if an increase of said total brightness level is determined.

4. (Previously Presented) The display device according to claim 1, further comprising a frame memory for storing input signals, representing said image, during a frame period for enabling a determination of the total brightness level of the image during the frame period.

Claim 5 (Canceled)

6. (Previously Presented) The display device according to claim 1, wherein the control means further comprise a look-up table for determining said fraction corresponding with said determined total brightness level.

7. (Previously Presented) The display device according to

claim 1, wherein said display pixels comprise a switch coupled to said control means for enabling light emission by said corresponding emissive display element for said fraction of said frame period.

8. (Currently Amended) A method for controlling a display panel having a plurality of display pixels with emissive display elements comprising the acts of:

determining a total brightness level for a previous frame of an image ~~to be previously~~ displayed in a previous frame period on said display, and

controlling a fraction of ~~said a~~ current frame period of an image to be displayed during which said display pixels emit light in dependence on said total brightness level for the previous frame.

9. (Currently Amended) An integrated circuit for controlling a display panel having a plurality of display pixels with emissive display elements, the integrated circuit comprising a duty cycle control arrangement for varying a fraction of a current frame

period of an image to be displayed during which said display pixels emit light in dependence on a total brightness level for a previous frame of an image ~~to be previously~~ displayed on said display panel.

10. (Previously Presented) The display device of claim 1, wherein the duty cycle control means include an adder configured to add brightness values of levels of the plurality of display pixels for the frame to form the total brightness level for the frame.

11. (Previously Presented) The display device of claim 10, wherein the duty cycle control means include memory configured to store the total brightness level for the frame.

12. (Previously Presented) The display device of claim 1, wherein the duty cycle control means include an adder configured to add luminance values of color components of a signal representing the image to be displayed to form the total brightness level for the frame.

13. (Previously Presented) The method of claim 8, wherein the

determining act includes adding brightness values of levels of the plurality of display pixels for the frame to determine the total brightness level for the frame.

14. (Previously Presented) The method of claim 13, wherein the determining act includes storing the total brightness level for the frame.

15. (Previously Presented) The method of claim 8, wherein the determining act includes adding luminance values of color components of a signal representing the image to be displayed to determine the total brightness level for the frame.

16. (Previously Presented) The integrated circuit of claim 9, wherein the duty cycle control means include an adder configured to add brightness values of levels of the plurality of display pixels for the frame to form the total brightness level for the frame.

17. (Previously Presented) The integrated circuit of claim 16, wherein the duty cycle control means include memory configured to

store the total brightness level for the frame.

18. (Previously Presented) The integrated circuit of claim 9, wherein the duty cycle control means include an adder configured to add luminance values of color components of a signal representing the image to be displayed to form the total brightness level for the frame.